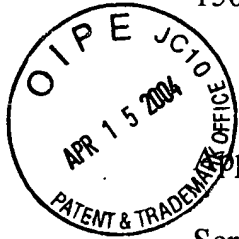


1503.68591

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Makoto Nakanishi )  
)  
Serial No. 10/692,533 )  
)  
Filed: October 24, 2003 )  
)  
For: PARALLEL PROCESSING )  
METHOD FOR INVERSE )  
MATRIX FOR SHARED )  
MEMORY TYPE SCALAR )  
PARALLEL COMPUTER )  
)  
Art Unit: 2127 )  
)  
Examiner: Not assigned )

*I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.*

*12 April 04*  
Date

*DMB*  
Registration No. 29,367  
Attorney for Applicant

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Please amend the application as follows: